

**Registration Form of
Workshop
On
“RF/Microwave
Fundamentals and FPGA
Debugging”
21st April 2012**

1. Name: _____
2. Designation: _____
3. Academic Qualification: _____
- 4 Branch: _____
5. Institute Name & Address: _____

6. Email ID: _____
7. Cell No.: _____
8. Teaching/Industry Experience: _____

Sign and Seal Institute

Signature of Applicant

Chief-Patron

Shri B. C. Patel, Chairman, NSVKM, Visnagar

Patron

Dr. H. N. Shah, Principal, SPCE

Convener

Prof. V. B. Vaghela, Associate Professor, EC

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Coordinators

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Registration is free of cost.

**Registration includes participation in all sessions,
softcopy of workshop material, lunch, refreshments
and certificate**

DATES AND VENUE:

Last date for Registration: 10th April 2012

Workshop Dates: 21st April 2012

**Venue: Seminar Hall (Department of Electronics &
Communication Engineering)**

Knowledge partner

 **Automation
System Engineers P. Ltd.**
4, SP Nagar, CG road Navarangpura Ahmedabad-6
Ph: 079-26409686 Email: agilent@asepl.in

 **Agilent Technologies**
Authorized Technology Partner

**One Day Workshop
On**

**“RF/Microwave Fundamentals and FPGA
Debugging”**

21st April 2012



Organized by

**Department of Electronics & Communication
Engineering**

Sankalchand Patel College of Engineering
Sankalchand Patel Sahakar Vidyadham,
Gandhinagar-Ambaji State Highway Link Road,
Kamana Char Rasta, Visnagar-384315

Dist: Mehsana, Gujarat, India

Web Site: www.spcevng.ac.in

ABOUT NSVKM

Nootan Sarva Vidyalaya Kelvani Mandal(NSVKM) is a charitable trust establish in 1952. The trust aims to impart high tech and professional education at college level in areas of science and technology, business and management and medical education.

The trust also aims to increase the higher and technical education opportunities in the region, and to meet the needs of the emerging society and improve the chances of employment, business and entrepreneurship and to contribute the higher standards of living and wealth generating activities for the society and the country.

The Institutions managed by the Trust

1. Sankalchand Patel College of Engineering (SPCE)
2. Swami Sachchidanand Polytechnic College (1st and 2nd Shift)
3. Narsinhbhai Patel Dental College & Hospital
4. Manish Institute of Management (MBA)
5. Manish Institute of Computer Studies (MCA)
6. Nootan Pharmacy College
7. Smt. S. B. Patel Institute of Business Management
8. Shri C. J. Patel College of Computer Studies
9. Nootan GNM Nursing School
10. Nootan General Hospital
11. Smt. S. S. Patel Nootan Science & Commerce College (Biotechnology, Microbiology, B.Com-English).
12. N. M. Nootan Sarva Vidyalaya (Primary, Secondary & Higher Secondary School)
13. R. J. Patel Secondary School, Kansa

ABOUT SPCE: Sankalchand Patel College of Engineering (SPCE), Visnagar, Gujarat, INDIA established in 1999 is a flagship institute of Nootan Group of Institutions, run by (NSVKM), Visnagar. SPCE is affiliated with GTU, Ahmedabad and its courses are approved by AICTE, New Delhi, INDIA.

SPCE accommodates 8 departments running 6 undergraduate courses namely Computer Engineering, Information Technology, Mechanical Engineering, Civil Engineering, Electronics & Communication Engineering, Electrical Engineering and 5 post-graduate courses namely Master of Engineering in Mechanical Engineering (CAD/CAM), Electronics & Communication Engineering (Wireless Communication Technology), Electrical Engineering (Power Systems), MCA and MBA. It has more than 150 skilled faculties and around 2500 students.

At Glance: Department of Electronics & Communication Engineering: Department of Electronics & Communication Engineering offer Undergraduate Programme of Bachelor of Engineering in Electronics & Communication and Postgraduate Programme of Master of Engineering in Wireless Communication Technology. The department has intake capacity of 120 students for undergraduate programme and 18 for the master programme.

OBJECTIVE OF THE WORKSHOP: As world leader in Test & Measurement Domain, **Agilent** has always aligned itself much before in time in terms of R&D and test and validation requirement for the latest and

upcoming technologies across digital as well as RF and microwave domain.

This workshop will upgrade knowledge, solutions that are relevant to Digital Design, Semiconductor field, Nanotechnology, RF technologies and will provide a platform to interact with some of the experts in this field.

COURSE PROGRAM

Topic of the workshop	Time
Registration	0930 to 0945
Introduction to EDUCATION LAB SOLUTION	0945 to 1015
RF/MW Test & Related Challenges	1015 to 1130
Tea break	1130 to 1145
EEsof- EDA Software for High Frequency & Hi-Speed System Monitoring and RF Circuit Design Application	1145 to 1230
Measurements on GSM Transmitter & Receiver	1230 to 1315
Lunch	1315 to 1400
Interpretation of Specifications of Most Used Instrument (Hand held Tools)	1400 to 1500
Analysis on MSO and Serial Bus Decode/Debug	1500 to 1545
Tea break	1545 to 1600
FPGA Debugging Solution.	1600 to 1700
Q/A , Feed Back form	1700 to 1730

GENERAL INFORMATION

The workshop is open to faculty members from AICTE approved / recognized engineering/diploma colleges, and industry participants.

Only 40 seats (First come First Basis) are available in this workshop. The application on the enclosed form, duly signed by the sponsoring authority, should reach at below mentioned address latest by 10th April 2012. Free accommodation is available in hostel.

MAILING ADDRESS: Coordinator: Prof. K. A. Shah, Department of Electronics & Communication Engineering, Sanakalchand Patel College of Engineering, Kamana Char Rasta, Visangar-384315. Gujarat, India