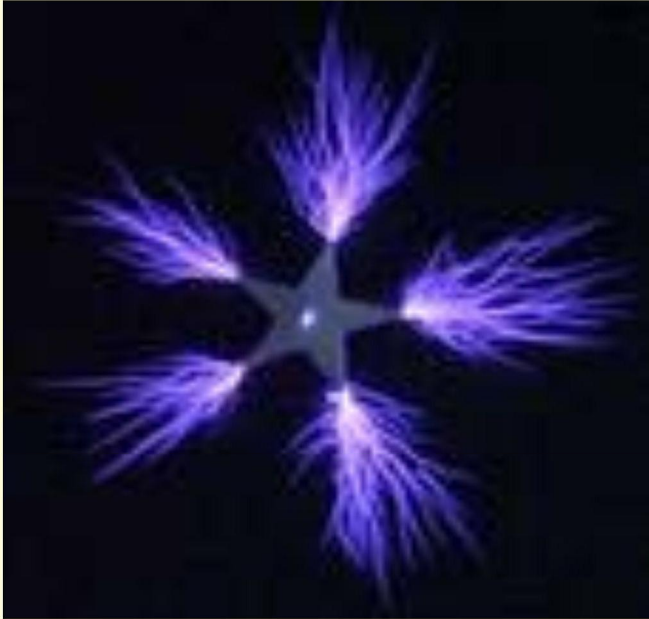


REGISTRATION FEE

Academic faculty	:	Rs. 600 /-
Industry & organization	:	Rs. 800 /-
Students & Research Scholar	:	Rs. 450 /-

Send registration form alongwith an appropriate demand draft (DD) drawn in favor of “**LDRP Institute of Technology & Research**” payable at **Gandhinagar**. Accommodation will be provided on request, if available.



It is to be noted that the number of seats for the course are limited to 50 and selection will be on first come first serve basis.

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President, Kadi Sarva Vishwavidyalaya and
Chairman, SVKM (Gandhinagar & Kadi)

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Prof. S.K.Mantrala

Registrar, Kadi Sarva Vishwavidyalaya

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Principal, LDRP-ITR, Gandhinagar

Prof. S.E.Mendhe

HOD EC Department

Coordinator

Prof. Reena M. Patel(9925721589)

rinapatel4444@yahoo.co.in

Mrs Kruti Thakore(9825632811)

kruti1_19@yahoo.co.in

Co-coordinator

Mrs Khushbu Joshi (9925011562)

Organizing committee

Mr Pratik Patel, Mrs Rachana Patel

Registration

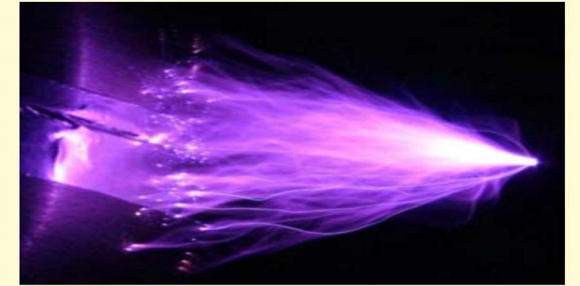
Mrs Rachana Modi, Mr Mayur Patel

ISTE Technically Supported self financed
A National Level Three Days

Work Shop

ON

ADVANCED TRENDS IN LOW POWER CONSUMPTION



During

21 – 23 July 2011

At

LDRP-ITR, Gandhinagar



Organized by

**Sarva Vidyalaya Kelavani Mandal's
Leelaben Dashrathbhai Ramdas Patel (LDRP)
Institute of Technology and Research
Nr. Kh-5 Circle, Beside ITI, Sector -15
Gandhinagar- 382015
www.ldrp.ac.in**

ABOUT US

Sarva Vidyalaya Kelavani Mandal is a Trust with 85 years of philanthropic history and is wedded to the promotion of education in the North Gujarat region. As part of its social mission and being sensitive to the emerging opportunities in the field of professional education the trust has diversified in the field of Graduate and Post Graduate studies in Management and Computer Studies. It has been a dream natured by the trust of establishing a state of art engineering college LDRP-ITR, which provides best possible facilities for nurturing young talents. The institute is currently offering AICTE approved five courses i.e. Computer Engg., Electronic & Comm. Engg., Electrical Engg., Mechanical Engg. and Information Technology.



ADVANCED TRENDS IN LOW POWER CONSUMPTION

In recent years power is big issue. To reduce the power at chip level, many techniques are used. In DSP pipe ling and parallel structure is used. At fabrication level with the use of scaling and different types of dielectric material can reduce power.

DETAILS OF WORKSHOP

Topics Likely To Be Covered:

- VLSI Introduction
- Parallel structure for low power
- Pipelining structure for low power
- Different dielectric for scaling of the devices
- Effects generated due to scaling
- New techniques introduced for scaling of the devices
- Scan chain insertion for reducing power
- Different types of algorithms used for low power
- Reduction of switching using algorithm for low power
- Don't care condition used for low power
- Basic knowledge of FPGA and ASIC
- Basic knowledge of fabrication techniques
- Construction of gates using MOSFET

The course is open for the faculties of EC/CET/EE, Dept of Degree/ Diploma Engg colleges ,P.G. students/Research fellows to upgrade their knowledge and skill.

EXPERTS:

Eminent national level experts from industries and academic institutions.

PRACTICALS & LAB FACILITIES

Computer Lab with high speed internet facilities at LDRP Institute of Technology and Research will be made available to each and individual participant.

REGISTRATION FORM

Work Shop on

ADVANCED TRENDS IN LOW POWER CONSUMPTION

21 - 23 July 2011

At Leelaben Dashrathbhai Ramdas Patel (LDRP), Institute of Technology and Research, Sector -15, Gandhinagar- 382015.

1. Name _____
2. Designation _____
3. Qualification _____
4. Institution / Organization _____

5. Mailing Address _____

Telephone _____ (M)
(With STD Code) _____ (R)

E- mail _____

6. Accommodation required (YES / NO) _____
7. Course Fee: Cash/Cheque/ DD
DD No. _____ Issuing bank _____
Dated _____ Rs. _____

Signature of the Applicant _____
Place _____ Date _____

IMPORTANT DATES

Training program is scheduled during **21 - 23 July 2011**
Last date for receiving applications: **15 July 2011**