Seat No.:	Enrolment No.

GUJARAT TECHNOLOGICAL UNIVERSITY

BE (SPFU) - SEMESTER-I-II (SPFU) - EXAMINATION – SUMMER 2017				
Su	bjec		Date: 16/06/2017	
Su	bjec	t Name: BASIC ELECTRONICS		
Time:02:30 PM to 05:00 PM			Total Marks: 70	
Ins	2.	ons: Attempt any five questions. Make suitable assumptions wherever necessary. Figures to the right indicate full marks.		
Q.1	(a) (b)	State and explain Kirchhoff's laws. Write short note on lumped circuit elements.	07 07	
Q.2	(a) (b)	Explain analogy between electrical and other non-electrical physical systems. State and explain theorem with suitable example.		
Q.3	(a) (b)	State and explain Norton theorem with suitable example. Draw and explain block diagram of operational amplifiers.		
Q.4	(a) (b)	Define op-amp. List practical properties of operational amplifiers. Draw and explain block diagram of computer system.		
Q.5	(a) (b)	Write short note on computer networks. List applications of operational amplifiers. Explain any one with circuit diagram.		
Q.6	(a) (b)	Explain wye-delta transformation with suitable example. Explain NAND gate as a universal gate.		
Q.7	(a) (b)	Draw and explain full adder circuit diagram. I. Convert a) (239) ₁₀ b) (13) ₁₀ in to binary number system II. Convert a) 1100110 b) 1110001 into decimal number system III. Convert a) 11011011 b) 11101010 into Hex code. IV. Find 2's compliment a) 110101 b) 1111001 V. State de Morgan's theorem.	07 07	

Convert a) (227)₈ b) (356)₈ into binary number system. Convert a) 11001101 b) 111000100 into octal number system

VI. VII.