		Enrolment No	
		GUJARAT TECHNOLOGICAL UNIVERSITY	
		BE - SEMESTER-VI • EXAMINATION - SUMMER • 2014	
Subje	ect C	Code: 160903 Date: 23-05-2014	
•		ame: Microcontroller	
U		30 am - 01:00 pm Total Marks: 70	
Instruc		<u> </u>	
		Attempt all questions.	
		Make suitable assumptions wherever necessary. Figures to the right indicate full marks.	
Q.1		*	07
	(b)	Draw the port structure of P0 and P1. Explain how P0 differs from P1	07
Q.2	(a)	Give the format of PSW register and explain the function of all flags. Also explain how PSW can be addressed at bit level	07
	(b)		07
	()	modifications are required to connect external memory to 8051? OR	
	(b)	Explain various addressing modes of 8051 with appropriate illustrations	07
Q.3	(a)		07
		M.L. 50H to 54H and 60H to 64H without using ANL instruction. Store the result in internal RAM locations 70H to 74H.	
	(b)	Write a program to toggle all bits of port 1. Put a time delay of 50 μS in between	07
		each issuing of data to port 1. Use timer 1 in auto reload mode and assume clock frequency to be 11.0592 MHz.	
		OR	
Q.3	(a)	Give the format of TCON and TMOD SFR's. Explain how hardware control of timers can be achieved through GATE bit.	07
	(b)	6	07
	()	be doubled by setting appropriate bit in PCON register.	
Q.4	(a)	Write a program using interrupts to simultaneously create 10 kHz and 500 Hz	08
		square wave on P1.7 and P1.6 respectively. Use timer 0 in mode 1 for creating 10	
		kHz wave and timer 1 in mode 2 for generating 500 Hz wave.	
	(b)	1 C	06
		bit data, 1 stop bit. Do this continuously	
Q.4	(a)	OR Assume that $PC = 4000H$ and $A = 02H$. Explain the reason why the data of 4002H	03
Ų. T	(a)	is not loaded in accumulator when the instruction MOVC A, $@A+PC$ is executed.	US
	(b)		08
	(c)	Explain the following assembler directives (i) ORG (ii) EQU (iii) END	03
Q.5	(a)	<u>•</u>	08
	<i>a</i> `	reading the code of key that has been pressed	Λ-
	(b)	Draw and explain in brief the interfacing circuit of unipolar stepper motor with 8051.	06
		8051. OR	
Q.5	(a)	With the help of a neat diagram explain the interfacing circuit of LCD with 8051.	07
.	(b)		07