Seat No.:	Enrolment No.

(i) Explain the concept of sampling and aliasing in sampled data system.

Subject Name: Digital Signal Processing and Processor

2. Make suitable assumptions wherever necessary.

3. Figures to the right indicate full marks.

Subject Code:183203

Instructions:

Q.1

Time: 10.30AM-01.00PM

1. Attempt all questions.

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(ii) Explain the symmetry property of the Fourier transform for real, real-odd, real-even discrete

Date:05/05/2015

Total Marks: 70

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	(b)	Compare energy and power signal. Determine whether following signal is energy or power and find it's value. $x(t) = e^{-at}u(t)$	07
Q.2	(a)	Find the Z- transform and ROC of the signal. $x(n) = 3[(2^n) - 4(3^2)]u(n)$	07
	(b)	Find if the following systems are linear, causal, stable, time-invariant and memory less. (i) $x_1(n) = \sum_{k=-\infty}^{\infty} x(k)$ (ii) $x_2 = x(n-n_o)$ (iii) $x_3 = x(n) + nx(n+1)$	07
		OR	
	(b)	Determine inverse Z transform of the following by partial fraction expansion. $x(z) = \frac{z+2}{2z^2 - 7z + 2}$	07
		If R.O.C is (i) $ z > 3$, (ii) $ z < 3$, (iii) $\frac{1}{2} < z < 3$	
Q.3	(a)	(i) Define ROC of Z transform and explain unilateral and Bilateral Z transform.	07
	, ,	(ii)Define term linearity and time invariance as applied to any discrete time system.	
	(b)	Find the impulse response for the system. $y(n) - \frac{1}{12}y(n-1) - \frac{1}{12}y(n-2) = x(n)$	07
		OR	
Q.3	(a)	Find the convolution of the two signals. When ROC (i) $ a < 1$	07
		$n \ge 0$ $x(n) = u(n) and h(n) = a^n u(n)$	
	(b)	Write difference between DFT and DTFT. State and prove Shifting, convolution and time reversal properties of DFT.	07
Q.4	(a)	Find the response of FIR filter with impulse response $h(n) = \{1,2,4\}$ to the input sequence $x(n) = \{1,2\}$ using circular convolution method.	07
	(b)	Explain the minimum-phase and all-pass decomposition of a system. OR	07

Q.4 (a) Given $x(n) = 2^n$ and N= 8 find x(k) using DITFFT algorithm.

- Q.5 (a) Develop a direct form I realization of the difference equation. $y(n) = b_0 x(n) + b_1 x(n-1) + b_2 x(n-2) + b_3 x(n-3) a_1 y(n-1) + a_2 y(n-2) + a_3 y(n-3)$
 - (b) Draw the block diagram of a digital signal processor with Harvard architecture and describe the **07** important differences with Von-Neumann architecture.

OF

Q.5 (a) The transfer function of analog filter is given below with Ts = 0.1 sec. Design the digital IIR filter using BLT.

$$H(s) = \frac{3}{(s+2)(s+3)}$$

using FFT algorithm?

(b) Explain window function used in FIR filter design also explain difference between FIR and 07 IIR filters in detail.

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