Seat No.:	Enrolment No
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GUJARAT TECHNOLOGICAL UNIVERSITY

BE – SEMESTER – VI (OLD).EXAMINATION – WINTER 2016

	•	ct Code: 160805 Date: 24/10/2016	
T	•	ct Name: Advanced Microprocessors 10:30 AM to 01:00 PM Total Marks: 70	
-11	isti uc	 Attempt all questions. Make suitable assumptions wherever necessary. Figures to the right indicate full marks. 	
Q.1	(a) (b)	Draw and explain the block diagram of 8288. Draw and Explain Flag register of 8086.	07 07
Q.2	(a)	Explain logical address, offset address and physical address by appropriate example.	07
	(b)	Draw and explain Minimum mode 8086 system. OR	07
	(b)	Draw and explain Maximum mode 8086 system.	07
Q.3	(a) (b)	Explain segment registers in detail. Draw and Explain Flag register of 8086. OR	07 07
Q.3	(a)	Draw and Explain the interfacing diagram of 8084 clock generator with 8086 microprocessor.	07
Q.4	(b) (a)	Explain MACRO in detail with format and example. Explain the following Pins of 8086. 1. HLDA 2. NMI 3. ALE 4. (INTA)' 5. READY	07 07
	(b)	Explain the rules for segmentation of 8086. OR	07
Q.4	(a)	Explain the following assembler directives of 8086. 1. DT 2. DUP 3. ORG 4. PROC 5. ENDP	07
	(b)	Explain the following instructions of 8086 with example. 1. PUSHF 2. XLAT 3. LDS 4. DAA 5. SHR	07
Q.5	(a) (b)	Write ALP to find average of ten 16-bit numbers. Write ALP to find maximum number from given array. OR	07 07
Q.5	(a)	Write ALP to copy the data from register Ax into Bx, Cx & Dx and DI using register mode.	07
	(b)	Write ALP to find minimum number from given array.	07
