Seat No.:	Enrolment No.
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Subject code: 140701

GUJARAT TECHNOLOGICAL UNIVERSITY

B. E. - SEMESTER – IV • EXAMINATION – WINTER 2012

Date: 27/12/2012

Subject Name: Microprocessor and Interfacing			
Time	e: 02	2.30 pm - 05.00 pm Total Marks: 70	
Instr	uct	ions:	
	1.	Attempt any five questions.	
	2.	Make suitable assumptions wherever necessary.	
	3.	Figures to the right indicate full marks.	
Q.1	(a)	Sketch the architectural block of 8085 microprocessor and explain each	07
Q.1	(a)	block in short.	U/
	(b)		07
	()	control bus.	
Q.2	(a)	What is instruction cycle? Explain using waveforms the fetch & execute	07
		cycle with reference of clock.	
	(b)	Distinguish between higher & lower level languages with an example of	07
		each. OR	
	(b)	1 6	07
Q.3	(a)	6 6	07
	(b)	flag bits affected.	
	(b)	Write an AL progam illustatating Arithmatic & logic group of instructions. OR	
Q.3	(a)		07
Q.S	(b)		07
Q.4	(a)		07
	()	three registers in nested loop.	
	(b)	•	07
		software interrupts.	
		OR	
Q.4	(a)		07
Q.4	(b)		07
Q.5	(a)		07
	(b)	example. Point out the valid and invalid instructions. Correct the invalid ones.	07
	(D)	MVI AB	U/
		LDA BD	
		MOV 05	
		ANI 0B	
		MOV H,L	
		LDAX B	
		STA C000	
		OR	. –
Q.5	(a)		07
	(b)	1	07
		(i) Programmable peripheral Interfacing device	
		(ii) Programmable timer/counter interfacing device (iii)Programmable DMA controller device	
		(iv)Programmable keyboard/display interfacing device.	
		(11)1 Togrammatic Reyboard/display micriacing device.	
