| Seat No.: | Enrolment No. |
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GUJARAT TECHNOLOGICAL UNIVERSITY

BE - SEMESTER-V • EXAMINATION - WINTER 2013

| Subject Code: 150701 Date Subject Name: Advance Processors | | | e: 27-11-2013 | |
|--|---|--|----------------|--|
| Time | Time: 10.30 am - 01.00 pm Total Marks: Instructions: | | 0 | |
| ingti u | 1. Att 2. Ma | tempt all questions. Also suitable assumptions wherever necessary. Solutions gures to the right indicate full marks. | | |
| Q.1 | (a) (b) | Draw and explain architecture of 8086 What is segmentation? List the various segment registers in 8086 and explain their role | 07 07 | |
| Q.2 | (a) (b) | Write an ALP to find factorial of given number Explain the addressing modes of 8086 with example OR | 07 07 | |
| | (b) | Explain Minimum mode operation of 8086. | 07 | |
| Q.3 | (a) (b) (C) | Differentiate between 16 bit and 32 bit Microprocessors. Find physical address for CS:IP 3000:123A H Explain different categories of Assembly Language Instruction Set | 04 03 07 | |
| Q.3 | (a) (b) | OR Explain different interrupts available in 8086. Explain Protected Virtual Address Mode of 80286 | 07 07 | |
| Q.4 | (a) (b) | What is descriptor table? What is it use? Difference bet GDT and LDT. Compare CISC Vs RISC | 07 07 | |
| Q.4 Q.4 | (a) (b) (c) | OR Explain paging mechanism of x86 family processors. (80386 & 80486) Explain the features of 80386. Explain Cache memory & Virtual memory | 07 03 04 | |
| Q.5 | (a) | Explain the Page Table and Page Directory Entry with example Explain addressing mode calculation for x86 family processor. (80386 & 80486) | 07 | |
| | (b) | Explain the architecture of SUN SPARC processor OR | 07 | |
| Q.5 | (a) (b) | What is Gate? Explain general Gate Descriptor format. Explain the Hyper Threading and core-2-duo regarding Intel pentium processor | 07 07 | |
