Enrolment No.____

GUJARAT TECHNOLOGICAL UNIVERSITY BE SEMESTER - V - EXAMINATION – WINTER 2013

Subject Code: 151006 Date: 09/12/2013 **Subject Name: Applied Electronics** Time: 10.30 am - 01.00 pm **Total Marks: 70 Instructions:** 1. Attempt all questions. 2. Make suitable assumptions wherever necessary. 3. Figures to the right indicate full marks. (a) Define the following terms: CMRR, Slew rate, Transducer, Photoemission Q.1 07 effect, Intrinsic standoff ratio(n), Duty cycle, Multiplexer (b) Draw and Explain the working of SMPS with required block diagram circuits and 07 wave forms. Enlist differences between linear and switch mode regulator. (a) What are the advantages of close loop configuration? Derive expression of Q.2 07 close loop gain for Non-inverting configuration of Op-Amp. (b) An op-Amp is used in inverting mode with $R_1=2K\Omega$, $R_F=30K\Omega$, $Vcc=\pm 15V$. 07 Calculate the output voltage for the given inputs:1) $V_{in}=100mV$ 2) $V_{in}=3V$. OR (b) Draw and explain Block diagram and voltage transfer characteristics of an Ideal 07 Op-amp. What is virtual ground concept? (a) Describe the steps to convert an Astable multivibrator in a square wave Q.3 07 oscillator using a diode. (b) State different types of temperature tranducers. Explain them in brief. 07 OR Q.3 (a) Derive the expression of pulse width for monostable multivibrator. Design a 07 monostable multivibrator using IC 555 for Vcc=12V and pulse width of 1msec. (b) Write a short note on piezoelectric transducer. 07 (a) What is Liquid Crystal Display? State and explain different types of LCD. 07 0.4 (b) Compare SCR with triac. Explain SCR with all necessary diagrams. 07 OR (a) Explain the optocoupler/Isolator in details. Also explain one application. 07 0.4 (b) Explain the working of DIAC(Diode A.C. Switch) with equivalent circuit and I-07 V characteristics of it. Q.5 (a) Explain the principle of Digital Storage Oscilloscope with block diagram. 07 (b) What is fuzzy logic? Explain CD technology with block diagram. 07 OR (a) What is sequential circuit? Compare sequential circuit with combinational Q.5 07 circuit. Explain A/D converter. (b) Explain the 8051 architecture in detail 07
