Suk Sub Tim	oject oject N e: 10 ruction		
	2.	Attempt all questions. Make suitable assumptions wherever necessary. Figures to the right indicate full marks.	
Q.1	(a) (b)	Explain various classes of computer architecture. Explain Memory Hierarchy.	07 07
Q.2	(a) (b)	Compare various platforms in terms of performance and efficiency. Explain subroutine nesting and the processor stack.	07 07
	(b)	Explain handling of multiple devices.	07
Q.3	(a) (b)	Explain Pipeline Organization in detail. Explain various addressing modes in instruction set architecture. OR	07 07
Q.3	(a) (b)	Compare and contrast RISC v/s CISC. Explain Cache Coherency.	07 07
Q.4	(a) (b)	Explain ARM characteristics and register ARM register structure. Explain hardware component selection and datasheet analysis. OR	07 07
Q.4	(a) (b)	Explain memory organization and register structure of Intel IA-32 architecture. Explain application of specific processor for microcontrollers.	07 07
Q.5	(a) (b)	Explain application of specific processor for multimedia. Explain virtual memory. OR	07 07
Q.5	(a) (b)	Explain asynchronous bus organization. Explain boot strapping process.	07 07
