## **GUJARAT TECHNOLOGICAL UNIVERSITY** MCA - SEMESTER-I • EXAMINATION – SUMMER 2013

| Subject Code: 610004 Date: 13-0<br>Subject Name: Fundamentals of Computer Organization |            |                                                                                                                                                                                                                                                                                              | 6-2013   |  |
|----------------------------------------------------------------------------------------|------------|----------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|----------|--|
| Time: 10:30am to 13:00pm Total Mai<br>Instructions:                                    |            |                                                                                                                                                                                                                                                                                              | :ks: 70  |  |
| Instru                                                                                 | 1.<br>2.   | s:<br>Attempt all questions.<br>Make suitable assumptions wherever necessary.<br>Figures to the right indicate full marks.                                                                                                                                                                   |          |  |
| Q.1                                                                                    | (a)        | <ul> <li>Do as directed:</li> <li>1) Define the terms: Computer, Resolution, Encoder, and Bootstrap Loader.</li> <li>2) Convert (46745)<sub>8</sub> into its binary equivalent.</li> <li>3) Multiply 11011111 with 111.</li> <li>4) Convert 11110101 to its gray code equivalent.</li> </ul> | 07       |  |
|                                                                                        | (b)        | <ul> <li>Do as directed:</li> <li>1) Add binary equivalent of 234 &amp; 567</li> <li>2) Subtract 234 from 189 using 1's complement subtraction.</li> <li>3) Convert (2ABD) 16 to its decimal equivalent.</li> <li>4) Convert (1101001.011)2 to its decimal equivalent.</li> </ul>            | 07       |  |
| Q.2                                                                                    | (a)<br>(b) |                                                                                                                                                                                                                                                                                              | 07<br>07 |  |
|                                                                                        | (b)        | • • •                                                                                                                                                                                                                                                                                        | 07       |  |
| Q.3                                                                                    | (a)        | Draw block diagram of digital computer & explain its all components.                                                                                                                                                                                                                         | 07       |  |
|                                                                                        | (b)        |                                                                                                                                                                                                                                                                                              | 07       |  |
|                                                                                        |            | 1) $\overline{A}.B + \overline{A}.\overline{B}.\overline{C} + A.B.\overline{C} + A.\overline{B}.\overline{C}$                                                                                                                                                                                |          |  |
|                                                                                        |            | 2) $\overline{A}(\overline{B}.C + \overline{B}.\overline{C}) + \overline{A}.B.\overline{C}$ OR                                                                                                                                                                                               |          |  |
| Q.3                                                                                    | (a)        |                                                                                                                                                                                                                                                                                              | 07       |  |
|                                                                                        | (b)        |                                                                                                                                                                                                                                                                                              | 07       |  |
| Q.4                                                                                    | (a)        | What is an Addressing Mode? Explain various addressing Modes with Examples.                                                                                                                                                                                                                  | 07       |  |
|                                                                                        | (b)        |                                                                                                                                                                                                                                                                                              | 07       |  |

- Q.4 (a) Write the Boolean expression (in sum of products form) for a logic 07 circuit that will have a 1 output when X=0, Y=0, Z=1 and X=1, Y=1, Z=0, and a 0 output for all other input states. Also draw the block diagram for the circuit.
  - (b) Write note on Magnetic disk as a Storage device. 07

## Q.5 (a) Write short note on:

- 1) Laser printer
- 2) OCR
- (b) What is Adder Circuit? Explain the working of Full adder with 07 necessary diagrams & tables.

OR

- Q.5 (a) Simplify the following expression using Karnaugh map method: 07
  - 1)  $\overline{A.B.C.D} + A.B.C.\overline{D} + A.B.\overline{C.D} + A.B.C.\overline{D} + A.B.C.D$
  - 2)  $\overline{A}.\overline{B}.\overline{C} + \overline{A}.\overline{B}.\overline{C} + A.\overline{B}.\overline{C}$
  - (b) Write short note on:
    - 1) Random scan display
    - 2) Bar code Reader

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