Seat No.: En	rolment No.
Seat 110 Eli	

## GUJARAT TECHNOLOGICAL UNIVERSITY

M. E. - SEMESTER – III • EXAMINATION – SUMMER • 2013

•		code: 730406 Date: 15-05-20	013
		Name: Peripheral System Design and Interfacing	
		.30 am – 01.00 pm Total Marks:	: 70
Instr	ucti	ons:	
		Attempt all questions.	
		Make suitable assumptions wherever necessary.	
	3.	Figures to the right indicate full marks.	
<b>Q.1</b>	(a)	Write the application of following bus in brief.	<b>07</b>
		S-100 bus, STD bus, Multi bus, GPIB, RS-232, Current loop, PCI bus.	
	<b>(b)</b>	Explain the function of each pin in detail of RS-232 9 pin connecter.	07
Q.2	(a)	Draw and explain the block diagram of 8257 DMA Controller.	07
	<b>(b)</b>		07
		OR	
	<b>(b)</b>	Write a short note on: Microprocessor Development Tools.	07
Q.3	(a)	Explain RS – 449 communication interface in detail.	07
<b>~</b>	(b)	-	07
	` '	parallel port communication.	
		OR	
Q.3	(a)	1. Explain centronics handshaking with necessary diagram.	<b>07</b>
		2. Explain bi directional mode of operation of parallel port.	
	<b>(b)</b>	Write a short note on parallel port mode in BIOS.	07
Q.4	(a)	What is Transparent addressing in CRT Controller? How it is	07
		implemented?	
	<b>(b)</b>	•	<b>07</b>
		Controller? Explain any two in detail.  OR	
$\Omega A$	(a)	-	07
Q.4	(a) (b)	Write a short note on PID controller.	07
	(0)	Write a short note on The controller.	07
Q.5	(a)	Classify the GPIB bus configuration and explain in detail.	07
•	<b>(b)</b>	• •	07
		2. Explain serial and parallel method of polling in GPIB.	
		OR	
Q.5	(a)	± • •	07
	<b>(b)</b>	Write a short note on: Programmable Logic Controller.	<b>07</b>

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