

**GUJARAT TECHNOLOGICAL UNIVERSITY**  
**M. E. - SEMESTER – I • EXAMINATION – SUMMER • 2014**

**Subject code: 710103N****Date: 17-06-2014****Subject Name: Distributed Operating System****Time: 02:30 pm - 05:00 pm****Total Marks: 70****Instructions:**

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.

- |   |   |           |
|---|---|-----------|
| <b>Q.1</b>  | <b>(a)</b> Compare and discuss workstation model and workstation ó server model.  | <b>07</b> |
|   | <b>(b)</b> Define: false sharing, thrashing, absolute ordering, orphan call, atomic transaction, location transparency. | <b>07</b> |
| <b>Q.2</b>  | <b>(a)</b> Compare remote procedure call and ordinary procedure call. Explain and discuss RPC model.                    | <b>07</b> |
|   | <b>(b)</b> Explain in detail Versatile Message Transport Protocol.  | <b>07</b> |
| <b>OR</b>   |   |           |
|   | <b>(b)</b> Explain in detail ATM protocol reference model.  | <b>07</b> |
| <b>Q.3</b>  | <b>(a)</b> Explain in detail OSI reference model.   | <b>07</b> |
|   | <b>(b)</b> Explain and discuss taxonomy of load balancing algorithms.   | <b>07</b> |
| <b>OR</b>   |   |           |
| <b>Q.3</b>  | <b>(a)</b> Discuss the relative advantages and disadvantages of sequential and release consistency models.              | <b>07</b> |
|   | <b>(b)</b> Explain in detail any one of the centralized clock synchronization algorithm.                                | <b>07</b> |
| <b>Q.4</b>  | <b>(a)</b> Compare and discuss stateful and stateless servers.  | <b>07</b> |
|   | <b>(b)</b> Explain in detail how deadlocks are handled in distributed system.   | <b>07</b> |
| <b>OR</b>   |   |           |
| <b>Q.4</b>  | <b>(a)</b> Explain desirable features of good migration mechanisms.   | <b>07</b> |
|   | <b>(b)</b> Explain different buffering techniques in IPC.   | <b>07</b> |
| <b>Q.5</b>  | <b>(a)</b> Explain desirable features of good distributed file system.  | <b>07</b> |
|   | <b>(b)</b> Explain process migration in heterogeneous systems.  | <b>07</b> |
| <b>OR</b>   |   |           |
| <b>Q.5</b>  | <b>(a)</b> Compare,   | <b>07</b> |
|   | 1) Micro kernel and monolithic kernel model.  |           |
|   | 2) Tightly coupled and loosely coupled system.  |           |
| <b>(b)</b> Compare and discuss election algorithms. | <b>07</b>   |           |

\*\*\*\*\*