Seat No.:	Enrolment No
GU	JARAT TECHNOLOGICAL UNIVERSITY

ME - SEMESTER- I (New course) • REMEDIAL EXAMINATION - SUMMER 2015 Subject Code: 2710702 Date: 14/05/2015 **Subject Name: Power Electronics** Time: 10:30 am to 1:00 pm **Total Marks: 70 Instructions:** 1. Attempt all questions. 2. Make suitable assumptions wherever necessary. 3. Figures to the right indicate full marks. **07** Q.1 Discuss the static and dynamic characteristics of SCR and Power BJT. Explain single quadrant, two quadrant and bidirectional operation of switches. 07 **(b)** Suggest power semiconductor devices capable to realize it. Q.2Explain working of Buck regulator with necessary circuit and waveforms and 07 (a) obtain necessary derivations for continuous mode of conduction. Explain working of Full bridge DC to DC converter with necessary circuit and **07** waveform. OR Explain the operation of a fly back converter with its relevant analysis. 07 **(b)** Q.3 Write short note on SMPS design criterion. 07 (a) Compare 120^o and 180^o modes of operation of 3-phase inverter. Explain 180^o 07 **(b)** mode of operation of 3-phase inverter with waveforms. OR Q.3 What is PWM Inverter principle? List the different pulse width modulation **07** (a) techniques used for inverters? Describe Multiple Pulse Modulation technique of PWM inverter with necessary waveforms and harmonic spectrums. Discuss space vector pulse width modulation (SVPWM) technique in brief. (b) 07 **Q.4** Explain operation of current source inverter with neat diagram. Differentiate it **07** (a) from voltage source inverter. Explain with neat circuit diagram and waveforms the principle of operation of (b) 07 three phase half wave cyclo-converters with circulating current mode. OR **Q.4** Describe the phase angle control technique for AC voltage controllers and **07** (a) Derive the expression for average and rms value of output voltage for single phase half wave AC voltage controller. Discuss 3-phase bidirectional delta-connected AC controller with necessary **07** circuit and waveforms for the delay angle of 120°. **Q.5** (a) Describe the necessity of Isolation of gate and Base drives and explain how 07 they are implemented using the pulse transformer and Opto Isolator. (b) Discuss steps illustrating design of inductor. **07** OR **Q.5** What does one mean by :Area Productø? Obtain the area-product for high **07** (a) frequency transformer used with forward converter. Explain the various protection circuits against dv/dt, di/dt and overvoltage in (b) **07** the driver circuit of power devices.
