Seat No.: Enrolment No.

GUJARAT TECHNOLOGICAL UNIVERSITY ME - SEMESTER-II EXAMINATION – SUMMER 2015

Subject Code: 2720212 Date: 01/00 Subject Name: Embedded System			/2015	
Tiı		02:30 PM to 05:00 PM Total Marks: 70		
	2.	Attempt all questions. Make suitable assumptions wherever necessary. Figures to the right indicate full marks.		
Q.1	(a) (b)	1	07 07	
Q.2	(a) (b)	What is Combinational logic design? Explain Combinational components for logic design.	07 07	
	(b)	OR Explain RT- level single purpose processor design.	07	
Q.3	(a) (b)	Explain State diagram templates for creating the data path.	07 07	
0.3	()	OR	0.5	
Q.3	(a)(b)	motor with a PWM.	07 07	
Q.4	(a) (b)		07 07	
Q.4	(a)	Write Short note on	07	
		(1) Cache mapping techniques, (2) Optimizing FSM.		
	(b)	What is DMA? Explain Peripheral to memory transfer with DMA.	07	
Q.5	(a) (b)		07 07	
		(2) Write short note on assembler directives of 8051 microcontroller. OR		
Q.5	(a)	Explain Internal Ram Structure of 8051 microcontroller. Explain Switching of Register Banks.	07	
	(b)		07	
