GUJARAT TECHNOLOGICAL UNIVERSITY ME - SEMESTER- III • EXAMINATION – SUMMER 2015

Subje	Subject Code: 730406 Date: 02/05/20		5
Subject Name: Peripheral System Design & InterfacingTime: 2:30 pm to 5:00 pmTotal Marks: 70Instructions:Total Marks: 70			
	2. I	Attempt all questions. Make suitable assumptions wherever necessary. Figures to the right indicate full marks.	
Q.1	(a)	What are limitations of Traditional Controller? Draw and explain Block diagram of PLC. Also list its advantages and area of	07
	(b)	applications. Explain different program development tools used in microprocessor based system.	07
Q.2	(a)	What is Centronics? Explain Centronics handshaking with timing diagram.	07
	(b)	Explain procedure for the parallel port to be used as 8-bits input. OR	07
	(b)	List different serial port is registers of PCs. Explain any three of them in detail.	07
Q.3	(a)	Explain Different Mode of IEEE 1284.	07
	(b)	Give answer of the following question.1). Explain RS-449 communication interface in detail.2). Explain Null Modem.	07
Q.3	(a)	OR Discuss GPIB signals and explain GPIB standard with the flow of	07
	(b)	data transfer handshake waveforms. Explain RS232 C standard with its voltage level and connectors. Also Mention its features and limitations.	07
Q.4	(a)	Draw the pin diagram of the 8257 DMA controller and explain the	07
	(b)	function of the DREQ, DACK, HLDA, HRQ. ADSTB. Draw and explain the block diagram of 8279 key board/display interfacing Controller. OR	07
Q.4	(a)	Explain in brief ISA, EISA, PCI and VESA-VL bus.	07
Q.4	(b)	Explain in brief1. Cross compilers 2. Automated Test Equipment.	07
Q.5	(a)	Explain general functions of CRT controller. Also explain	07
	(b)	transparent addressing in CRT controller in brief. What is frame synchronism using RES? Explain it in detail With timing diagram. OR	07
Q.5	(a)	Draw and explain Block diagram of PC based data acquisition	07
	(b)	system. Write a short not on PID Controller.	07