Seat No.:	Enrolment No.

## GUJARAT TECHNOLOGICAL UNIVERSITY ME – SEMESTER-1 (NEW) EXAMINATION – WINTER 2016

Subject Code: 2710311 Date:04/01/2017 **Subject Name: Embedded system for Instrumentation** Time: 2:30 pm to 5:00 pm **Total Marks: 70 Instructions:** 1. Attempt all questions. 2. Make suitable assumptions wherever necessary. 3. Figures to the right indicate full marks. **Q.1** Explain ARM's Programmer model in details 14 **Q.2** Explain how interrupts are managed by Cortex M processors in brief. 07 (a) Write an Assembly language program for reversing bit orders in given register. **07 (b)** Write an Assembly language program for divide without using DIV instruction. **07** (a) Explain the features of NVIC in details 07 **Q.3** Write an Assembly language program for finding a maximum within a array and 07 storing in other array 07 0.3 Explain Features of Cortex M processors in details. Write an assembly language program for finding 2's complement of a number **(b)** 07 What is lazy stacking? Enlist the different scenario for the same and explain **Q.4** (a) 07 any two along with the key element. Enlist the various types of data types available in in ARM Architecture Including Cortex-07 **(b)** M Processors with their size and range. **Q.4** Explain Bit-band operations and its advantages 07 (a)

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Write an assembly language program for counting number of leading zero in a

Write an ASM for reading different number from memory location and combine

Write an assembly language program for moving number from array if the

them in other memory location using LSL-LSR instruction. **(b)** Explain various types of shift and rotate instructions with a neat sketch.

(a) Explain in brief what happens when the microprocessor resets.

number is less than  $0 \times 77777777$ .

07

07

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07

07

**(b)** 

(a)

**Q.5** 

0.5

given number