Sea	t No.:	Enrolment No	_	
		GUJARAT TECHNOLOGICAL UNIVERSITY ME – SEMESTER III (NEW) – EXAMINATION – WINTER-2016		
			5/10/2016	
Tir	ne: 02 cruction 1. 2.	2:30 pm to 05:00 pm Total Marks: 7	70	
Q.1	(a) (b)	What is Clean Room and why it is required? Write down the clean room classification as per federal standard 209 D. Give the Comparison between Silicon and Germanium. Which is best suited for wafer fabrication.	07 07	
Q.2	(a)	List down the different size of wafer and how we can identify it. What are the major obstacles to move from 300mm to 450mm size wafer?	07	
	(b)	What is oxidation? Why it is needed? Explain the difference between transport limited and reaction rate limited oxidation of silicon. OR	07	
	(b)	Discuss effect of sodium contamination on the performance of pMOS and nMOS Transistors.	07	
Q.3	(a)	In VLSI why E-beam lithography is widely used? What is PMMA? Discuss E-beam lithography.	07	
	(b)	What is diffusion? Explain Predeposition and Drive-In techniques to introduce phosphorous impurity in p-type<100> silicon Substrate. Explain Importance of O ₂ gas mixed with nitrogen gas.	07	
Q.3	(a)	OR What is the difference between positive and negative resist? What is the difference between emulsion and chrome mask?	07	
	(b)	Explain role of O ₂ in furnace technique. What do you mean by solid solubility?	07	
Q.4	(a) (b)	Explain why shallow junction is needed for smaller geometry devices? Why AlSi and ALSiCu are used in place of pure Al in fabrication of VLSI Circuit? OR	07 07	
Q.4	(a) (b)	How RC time constant is decreased in multilevel metallization scheme? Explain RF Sputtering technique to deposit AlSiCu alloy for shallow junction devices.	07 07	
Q.5	(a)	What is wet and dry etching? Which technique is generally used to get	07	

(a) Explain electrostatic discharge damage in IC. Discuss protection network in the

(b) Explain Junction Spiking and Electromigration in pure Al film.

(b) Explain the different types of wafer based on wafer cut.

Q.5

Q.5

anisotropic etching? Why?

Circuit.

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