	Seat N	No.: Enrolment No		
		GUJARAT TECHNOLOGICAL UNIVERSITY		
	6 k :	M. E SEMESTER – II • EXAMINATION – WINTER • 2014		
	•	ect code: 1720107 Date: 05-12-2014 ect Name: Embedded System		
	•	e: 02:30 pm - 05:00 pm Total Marks: 70		
	Instructions:			
1. Attempt all questions.				
		 Make suitable assumptions wherever necessary. Figures to the right indicate full marks. 		
Q.1	(a) (b)	Discuss microcontroller and ccdpp/fixed point dct Design a cold drink machine controller, given that a cold drink costs 35/- Rs.	07 07	
	(0)	Machine accepts quarters only Draw a black ó box view, come up with a state diagram and state table, minimize the logic and draw the circuit.	07	
Q.2	(a)	Draw and explain functional block diagram of 8051 microcontroller.	07	
	(b)	Given 1 X 10 ⁵ Hz crystal controlled oscillator and 64 bit and any number of 16 bit terminal count timers, design a real-time clock that outputs the date and time down to milliseconds.	07	
		OR		
	(b)	Explain in detail 8051 register banks and stack	07	
Q.3		Write a code to generate a ramp using the DAC (DAC0800)	07	
	(b)	List and Explain disadvantage of Multilevel Bus Architectures OR	07	
Q.3	(a)	Attempt following:	07	
		(i) What are the functional requirements of embedded system?(ii) Give some examples for sophisticated embedded systems.		
	(b)	Explain Arbitration using daisy-chain configuration in detail.	07	
Q.4	(a)	Write a C program to count in BCD from 00 to 99.(use the 2 digit	12	
	(b)	BCD decoded 7 segment display as the output. List difference between DIP switch and PORT	02	
	(0)	OR	02	
Q.4		Explain working of all important functional unit of Keypad Controllers What is the need for IDE in an Embedded Architecture?	07 07	
•	(b)		07 12	
Q.5	(a)	Write a C program to switching ON and OFF individual LEDs with software delay. Use the eight independent LEDS.	12	
	(b)	What are the forms of timer?	02	
Q.5	(a)	OR Explain in detail about UART single ópurpose processor as an FSMD	07	
~~··	(b)	List the hard ware units that must be present in the embedded systems.	07 07	
