Enrolment No.

GUJARAT TECHNOLOGICAL UNIVERSITY PDDC - SEMESTER-IV • EXAMINATION – SUMMER • 2014

Subject Code: X41103 Date: 21-06-2014 **Subject Name: Integrated Circuits and Applications** Time: 10:30 am - 01:00 pm **Total Marks: 70 Instructions:** 1. Attempt all questions. 2. Make suitable assumptions wherever necessary. 3. Figures to the right indicate full marks. Q.1 (a) Derive the equations of closed loop voltage gain, input resistance and output 07 resistance with feedback for voltage series negative feedback amplifier. (b) Explain Biquad filter design in detail. 07 0.2 (a) The 741C op-amp having the following parameters is connected as a non-inverting 07 amplifier with $R_1 = 2.7k\Omega$ and $R_F = 15k\Omega$: A = 200000, Ri = 2M\Omega, Ro = 75\Omega, fo = 5Hz, supply voltage = $\pm 15V$, output voltage swing = $\pm 13V$. Compute the values of AF, RF, ROF, fF, and VOOT. Explain RC-CR transformation in detail. 07 **(b)** OR 07 **(b)** Design a bandpass filter with a center frequency at $\omega_0 = 1000 \text{ rad/sec}$, a bandwidth of 200 rad/sec, and a maximum gain of 1, using the biquad circuit. (a) Explain differential amplifier with two op-amps in detail. 07 0.3 (b) Explain summing, scaling and averaging amplifier using op-amp inverting 07 configuration in detail. OR Explain DIDO amplifier in detail. **Q.3** 07 (a) (b) Explain voltage-to-current converter with grounded load in detail. 07 Explain differentiator using op-amp in detail. 0.4 07 (a) Explain square wave generator using op-amp in detail. 07 **(b)** OR (a) Explain Schmitt trigger circuit using op-amp in detail. 07 Q.4 Explain Monostable Multivibrator using 555 timer in detail. 07 **(b)** (a) Explain operating principles and phase detector of phase-locked loop in detail. 07 Q.5 (b) Explain adjustable voltage regulators in detail. 07 OR (a) Explain fixed voltage regulators in detail. **Q.5** 07 Explain astable multivibrator using 555 timer in detail. 07 **(b)**
