Seat No.:	Enrolment No.

GUJARAT TECHNOLOGICAL UNIVERSITY

PDDC- SEMESTER III- • EXAMINATION -WINTER- 2016

Subj	ect (Code: X30901 Date:30/12/2016	
Subj	ect l	Name: Basic Electronics	
Time	e:10	:30 AM to 1:00 PM Total Marks: 70	
Instru			
	2.	Attempt all questions. Make suitable assumptions wherever necessary. Figures to the right indicate full marks.	
Q.1	(a)	Explain How Zener Diode can be works as a Voltage Regulator.	07
	(b)	Explain the Followings: 1) Reverse recovery time of diode 2) Voltage equivalent of temperature 3) Diffusion Current 4) Mean life time of carrier 5) Mobility of electron 6) Electron volt 7) Electric Field	07
Q.2	(a)	Explain Current Density and conductivity of Extrinsic semiconductor material.	07
	(b)	Explain the formation of barrier potential in open circuited PN junction diode. Also derive the expression for barrier potential. OR	07
	(b)		
Q.3	(a)	Explain half wave voltage doubler circuit using diode.	07
	(b)	Explain the different types of clipping circuits. OR	07
Q.3	(a)	Explain ideal diode with characteristics also Explain all factors affecting the Load Line and Q-point.	07
	(b)	Explain full wave center tap rectifier with waveform and derive the equation of $V_{\text{\rm dc}}$, efficiency and ripple factor.	07
Q.4	(a)	Explain CE transistor configuration with input and output characteristics. Also indicate different regions.	07
	(b)	State the need of biasing. Discuss voltage divider bias circuit and mention its advantages. OR	07

Q.4	(a)	Explain h-parameter for CB configurations.	07
	(b)	Define thermal runway. Derive necessary condition to avoid thermal runway.	07
Q.5	(a)	Explain construction of p-channel depletion type MOSFET with characteristics.	07
	(b)	Explain with neat circuit diagram, the working of a transformer coupled class A power amplifier.	07
		OR	
Q.5	(a)	Explain the principle of operation of JFET. Also compare FET with BJT.	07
	(b)	State the role of voltage regulators in power supplies? Discuss working of a series voltage regulator.	07
