



## GUJARAT TECHNOLOGICAL UNIVERSITY

2<sup>nd</sup> Floor, Admission Building, L.D. College of Engg. Campus, Navrangpura,  
Ahmedabad-380015, Gujarat.



### ***JOINTLY ANNOUNCES THREE NEW PROGRAMMES***

**M.E. Programme in VLSI & EMBEDDED SYSTEMS DESIGN**

along with **SEER AKADEMI**

**M.E. Programme in 1) Wireless & Mobile Computing and**

**2) IT Systems and Network Security**

along with Centre for Development of Advanced Computing (CDAC)

**Academic year 2011-2012**

#### **Salient Features:**

##### **1) VLSI & EMBEDDED SYSTEMS DESIGN** along with **SEER AKADEMI**

This is a Unique US-Style Post Graduate Academic Program of international standards which brings out a world class curriculum from Synopsys University program (Armenia). Our Program emphasizes more on Industry oriented skill development with hands-on practicals to lecture ratio of 2:1. This program provides 24\*7 access to our State of the Art labs. All the lectures are delivered through local expertise and enables remote teaching with collaborative learning through WebEx, VPN, and Screen Sharing etc. Our program encourages effective interaction with Industry professionals and University experts across the globe. Special guidance is given through a team of our qualified teaching assistants with personal interaction and Social Media. We focus on strong skill development, building new Entrepreneurs and increase visibility among employers.

The program will be conducted at Viswakarma Government Engineering College (VGEC), Sabarmati-Koba Highway, Chandkheda, Ahmedabad – 382424 where the new GTU building is coming up and IIT Gandhinagar is temporarily located.

**Eligibility Criteria:** Should possess B.E. / B.Tech Degree (Electronics and Communication) and having valid GATE score or those candidates who have appeared in Common Entrance Test 2011 conducted by ACPC for M.E programmes

**Requirement of Course:** Students are required to bring **LAPTOP** (i3 processor or higher version with broad band data card) during the course. GTU may assist, If required, to get the loan from bank

##### **2) Wireless & Mobile Computing** and **3) IT Systems and Network Security**

These two M.E. programmes have been designed to be practice oriented. In each of these programmes every student will be required to learn at frontiers of technology with a focus on applied research.

C-DAC is a scientific society of Department of Information Technology, Ministry of Communication and Information Technology, Government of India. C-DAC was established to develop India's own super computer. The programmes are designed by GTU professor in consultation with the C-DAC R&D and industry experts. The selected students shall undergo the theory and lab sessions of first year at GTU campus. C-DAC shall offer the live project work for the thesis at its Research and Development Centre, Pune or Bangalore during the second year of M.E. Programme.

The program will be conducted at Bhaskaracharya Institute For Space Applications and Geo-Informatics (BISAG), Nr. CH 'O' Circle, Indulal Yagnik Marg, Gandhinagar- Ahmedabad Highway, Gandhinagar, 382 007.

**Eligibility Criteria:** Should possess B.E. / B.Tech Degree (CE/IT) and having valid GATE score or those candidates who have appeared in Common Entrance Test 2011 conducted by ACPC for M.E programmes

#### **For the above three programs:**

**Admission Procedure:** Admission procedure shall be carried out by ACPC

**Duration of Course:** Full time two year course (4 semesters)

**Fee Structure:** Subsidized fee ₹ 50, 000/-per year for all the above three courses

**Facilities:** 24\*7 access to our State of Art labs and free Wi-Fi Internet facility available in classroom and labs.

**Commencement of classes at respective location of the programme and Orientation Programme will be on 12<sup>th</sup> September 2011 at Viswakarma Government Engineering College (VGEC), Chandkheda, Ahmedabad.**

Please note that after about 6-8 months when the GTU building is ready at Chandkheda, Ahmedabad, all these three programs will be moved to the GTU building.

**I/C Registrar**