



NATIONAL CONFERENCE ON EMERGING VISTAS OF TECHNOLOGY IN 21ST CENTURY

14th & 15th April, 2012

Organized By
GUJARAT TECHNOLOGICAL UNIVERSITY,
AHMEDABAD



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Parul Institute of
Engineering & Technology

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The Indian Journal of Technical Education &
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Technical Sessions & Paper Presentation Schedule

Date : 14th April, 2012 (Day-1)

“ECO-FRIENDLY COMMUNICATION”

DATE	Time	TOPIC	VENUE	
14 th April 2012	8:30 to 9:30	Registration & High Tea	Ahmedabad Management Association, Dr. Vikram Sarabhai Marg, University Area, Ahmedabad, Gujarat	
	9:30 to 11:30	Inaugural Function.		
	11:30 to 12:30	Lunch Break		
	12:30 to 13:00	TECHNICAL SESSION – I Prof. Dr. S. Mukherjee , I.I.T Roorkee , Speaker and Chair Person - Green Communications		
	13:01 to 14:30			Performance Analysis of Discrete Cosine Transform Based Digital Image Watermarking <i>Bhavna S. Patel, Kamal Kumar Verma</i> <i>Rajasthan Institute of Engineering & Technology, Jaipur</i>
				Novel Image Segmentation for Aircraft Type Recognition Using Hausdorff Distance <i>Suhas BBhise, Ajay B. Patil</i> <i>Rajarambapu Institute of Technology, Islampur</i>
				Analysis of Space Vector Modulation Technique <i>M. A. Lokhandwala, Hina B. Chandwani, Y. P Kosta</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>
				Development of Enhanced Bi-directional Audience Opinion Acquisition System <i>Madhuriben D Thakkar, Ila D Parmar</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>
				Various Methodologies for Steganography: It's Detection and Evaluation <i>Shashank S. Mitta, Kiritkumar R.Bhatt, Reena M.Patel</i> <i>Sardar Vallabhbhai Patel Institute of Technology, Vasad</i>
				Dual Rate Speech Coder ITU G.723.1 Implementation and Simulation <i>Unnati P. Shah, Kamlesh D Sangani</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>
				Digital Watermarking of a motionless image using DWT <i>Arvind R. Yadav, Marmik H. Bhavsar</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>
				Developing DSP Laboratory by Implementation of Speech Processing Algorithm Using CCS And DSK6713 Based On Tms320c6713 Digital Signal Processor <i>Hardik S. Kathiriya, Ankil K. Parmar</i> <i>Shri Sad Vidya Mandal Inst. of Tech., Bharuch</i>
				Orientation Computation of Fingerprint Image <i>Kinnari Shah, Arvind R. Yadav, Ekata Mehul</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>
				Modeling of LV SYSTEM Standardized Input / Output in CONTROCAD: An Experimental Study <i>Roma K.Patel, Sanjeev Gupta</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>



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	14:30 to 14:45	TEA BREAK	
		TECHNICAL SESSION – II Dr. Mehul S Raval- DAIICT, Gandhinagar Speaker and Chair Person – Idea of Simultaneous Sampling and Compressing	
		Robust Adaptive Image Security Using Mixed Frequency Domain <i>Mohammed G. Vayada, Anil C. Suthar, Arvind R. Yadav</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		A Mel- Filter and Kepstrum Based Algorithm for Noise Suppression in Cochlear Implants <i>Mitul M. Patel</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Analytical Change in Power Spectrum Density in Speech for Normal and Depressed Person <i>Navin Khambhala, Hardik Mewada, Anil C. Suthar</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		ITU-T G.729.1 Wideband Speech Coder <i>Manish Jaywant Pavar, Mitul M. Patel</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
14 th April 2012	15:15 to 17:00	Designing and Implementation of Licence Plate Recognition Using Image Processing <i>Jwolin M. Patel, Arvind R. Yadav</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	Ahmedabad Management Association, Dr. Vikram Sarabhai Marg, University Area, Ahmedabad, Gujarat
		Review Paper on ‘Text Dependent Speaker Recognition System Using K-Mean Algorithm’ <i>Nitinkumar C. Patel, Kalpesh R. Jadav</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Vision System Based Robot for Arrow Detection <i>Vaibhav Patel, KalpeshJadav</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		An Efficient Implementation of Medical Image Compression Using Cavlc Algorithm <i>Harsha Jairaman, Bhavini P. Parmar</i> <i>Shri Sad Vidya Mandal Inst. of Tech., Bharuch</i>	
		BioMEMS: Present and Future <i>Anil B Patil , Manoj S Kavedia</i> <i>Smt. S. H. M. Institute of Technology, Maharashtra</i>	
		A Novel Data Hiding Technique for LSB Steganography <i>Nakrani Miteshkumar B., S.K.Chhotaray</i> <i>Sardar Vallabhbbhai Patel Institute of Technology, Vasad</i>	



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Date : 15th April, 2012 (Day-2)

“ECO-FRIENDLY COMMUNICATION”

DATE	TIME	TOPIC	VENUE
	8:30 to 09:00	TEA	
	09:00 to 09:30	TECHNICAL SESSION-III Dr. R. A. Thakker, VGEC, Chandkheda , Speaker and Chair Person – Organic Electronics and RFID	
15 ^h April 2012	09:31 to 11:15	Review of Different Techniques of Speaker Recognition <i>Dhrumil A. Patel , Maulin M. Joshi</i> <i>Sarvajanik college of Engineering and Technology, Surat</i>	Ahmedabad Management Association, Dr. Vikram Sarabhai Marg, University Area, Ahmedabad, Gujarat
		Study of GA, PSO, PSO-EA & ABC Algorithms <i>Vimal H. Nayak, Haresh A. Suthar, Umang H. Soni</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Increased The BER with the Modified DES Algorithm <i>Rita R. Padvi, Hina R. Patel</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Adaptive Modulation & Coding with MIMO Techniques in IEEE 802.11n <i>Hetal N. Shah, Hardip K. Shah</i> <i>Dharmsinh Desai University, Nadiad.</i>	
		Survey of Improving TCP Performance Over Wireless Networks Using Loss Differentiation Algorithm <i>Nikita B. Dalwadi, Purvang D. Dalal</i> <i>Government Engineering College, Surat</i>	
		Algorithms Developed for Advance Wireless Control Unit <i>Chaitali S. Panchal , Payal D. Kansagra</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		FPGA Implementation and Comparative Analysis of Fragile and Robust Watermarking Algorithm <i>Vidita B. Kanani, A. I. Darvadiya</i> <i>Sardar Vallabhbhai Patel Institute of Technology, Vasad</i>	
		A New Design of Low Noise and High Speed SRAM Cell <i>Kanan Patel, Y.B.Shukla</i> <i>C.U. Shah. College of Engg & Tech, Wadhwan</i>	
		A Survey on Implementation of VGA on FPGA <i>Ashish B. Pasaya, Kiritkumar R. Bhatt</i> <i>Sardar Vallabhbhai Patel Institute of Technology, Vasad</i>	
		Low Voltage Resized Design of Programmable Current Mirror <i>Rajashree N. Jetani , Devang Shah, K.R.Bhatt</i> <i>Sardar Vallabhbhai Patel Institute of Technology, Vasad</i>	



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	11:15 to 12:00	LUNCH BREAK	
	12:00 to 12:30	TECHNICAL SESSION – IV Prof. Ved Vyas Dwivedi -Noble Group of Institutes Speaker and Chair Person – Optimization of Electronics Devices: Design Issues	
15 ^h April 2012	12:31 to 14:15	Dual Band Patch Antenna Design for Wireless LAN <i>N.J. Bathani, J. V. Dave</i> <i>L.D. College of Engineering, Ahmedabad</i>	Ahmedabad Management Association, Dr. Vikram Sarabhai Marg, University Area, Ahmedabad, Gujarat
		High Gain and Wide Bandwidth Microstrip Patch Antenna <i>Utkarsh V. Shah, Jagdish M. Rathod, Ila D. Parmar</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Prediction of Earthquake & Tsunami Using Remote Sensing & GIS <i>Shweta A. Gupta</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Energy Detection Algorithm in Cognitive Radio Network under Noise Uncertainty and Dynamic Threshold <i>Khyati P Vachhani, Vimal Prajapati</i> <i>Kalol Institute of Technology</i>	
		Design and Performance Analysis of Orthogonal Space Time Block Code <i>R. S. Modi, M.A.Lokhandwala P. A. Labwani, K. R. Parmar</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Performance Comparison of RS Codes Using MFSK and MPSK Modulation <i>Parmar Kokila, M.A.Lokhandwala, M.B.Potdar</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Wireless Energy Metering Using ZigBee and GSM Technologies <i>Yashesh K.Pujara, Gaurav G. Shah, Jignesh Patoliya</i> <i>Charotar Institute of Technology, Changa</i>	
		Design issues in Routing Algorithms for Wireless Sensor Networks <i>Prashant J Bagga, Anuj Chadha</i> <i>Nirma University, Ahmedabad</i>	
		A Novel Approach of Communication in Microgrid <i>C. V. Pandya, P. H. Tandel, I. N. Trivedi</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Special Types of Switched Beam Smart Antenna System- Butler Matrix <i>Khyati D. Chavda, A.R.Charania, Neha Dabhi</i> <i>Sardar Vallabhbbhai Patel Institute of Technology, Vasad</i>	



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	14:15 to 14:30	TEA BREAK	
	14:30 to 15:00	TECHNICAL SESSION - V Mr. Prajose John, e-Infochips Ltd., Ahmedabad Speaker and Chair Person – FEMTOCELL Works IN 4G Technology	
15 ^h April 2012	15:01 to 16:30	Analysis on SEU Mitigation Techniques for FPGA <i>Hemal R. Patel, Hardik Mewada, Rushabh Shah</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	Ahmedabad Management Association, Dr. Vikram Sarabhai Marg, University Area, Ahmedabad, Gujarat
		Implementation of Time of Flight and Mass by Charge Ratio Measurement Algorithms for Mass Spectrometer <i>Sandeep J. Rajput, , Amish B. Shah, Arvind R. Yadav</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Performance Comparison of Noise Tolerant Domino Logic Configurations <i>Mitesh J. Limachia, Sumit K. Patel, Nikhil Kothari</i> <i>Dharmsinh Desai University, Nadiad</i>	
		Analysis of Barcode Reading System <i>Jay A. Patel , S.K.Chhotaray</i> <i>Sardar Vallabhbhai Patel Institute of Technology, Vasad</i>	
		Performance Evaluation of Modified Flood Fill Algorithm Using Microcontroller Based System <i>Anjali G. Chauhan, Mr .Haresh A. Suthar</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		A Review Paper on Semiconductor Memory Architectures <i>Tanmay H. Bhatt</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Designing and Simulation of DDR2 Physical Layer Sub Blocks <i>Pratik B. Vihol, Tanmay H. Bhatt</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Analytical Study on “Field Programmable Gate Array” for Digital System Design <i>Umang H. Soni, Anuradha P. Gharge, Vimal H. Nayak</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Designing of a 4Kb SRAM Memory Using Negative Word Line Scheme for 180nm Technology <i>Ashish V. Gupta</i> <i>Parul Institute of Engineering & Technology, Vadodara</i>	
		Multi Level Logic & Its Applications to Interface With Digital System <i>Utsav Kumar Malviya, Renu Dubey, Sumant Katiyal</i> <i>GGITS, Jabalpur</i>	
16:31 to 17:00	Valedictory Function		