

# *Gujarat Technological University*

## **Fault Diagnosis of Analog Integrated Circuits**

Organized by the 2013 Virtual Academy of Indo US Collaboration for Engineering Education  
(IUCEE)

Tue, Sep 3, 2013 3:00 PM - 4:00 PM IST

**Registration Web Link:** <https://www3.gotomeeting.com/register/845787070>

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### **Outline:**

The field of integrated circuit (IC) has undergone remarkable changes over the past decade. ICs incorporating both digital and analog function have become more and more popular in semiconductor industry. These combined circuits are called “Mixed Signal Circuits”. Fault diagnosis and testing of IC has grown into a special field of interest in the semiconductor industry. The methodologies of testing digital circuits are well established. However, the methodologies for fault diagnosis of analog ICs are relatively unexplored. Some of the reasons for this under development are lack of proper fault model of the devices operating in the continuous time domain, multiple value of the signal at each node of the circuit, etc. This tutorial on Fault Diagnosis of Analog Integrated Circuits is primarily intended for students at graduate level and practicing field engineers in the area of analog and mixed-signal IC testing. The prime objective is to provide an insight into the different fault diagnosis techniques of analog ICs