



GUJARAT TECHNOLOGICAL UNIVERSITY

No. GTU/TIFDP/4138

Date: 10.06.2014

Faculty Development Training Program On

1. Digital signal Processing,
2. Analog System Design Using ASLK and
3. Embedded System Design Using MSP430

Gujarat Technological University announces series of Faculty Development Training programs on Digital signal processing, Analog System Design Using ASLK and Embedded System Design Using MSP430 in association with **eInfochips Institute of Training and Research Academy (eiTRA)**, Ahmedabad. Main objective to organize this workshop is to understand the concepts of DSP, Analog and Embedded systems and implementing the same using DSP processors, ASLK kit and MSP430.

Who can participate?

- Faculties of Electrical/Electronics/Electrical and Electronics/ Instrumentation and control Engineering institute
- Familiar with Basic fundamentals of DSP
- Basic knowledge of signals and system and MATLAB

In-class Training schedule:

9:00 AM – 5:00 PM

Training Dates:

Digital Signal Processing

20th & 27th June, 2014

Analog System Design using ASLK

21st & 28th June, 2014

Embedded System Design using MSP430

30th June, 2014

Venue for the Workshop:

Gujarat Technological University, L. D. College of Engineering Campus, Navrangpura, Ahmedabad-380015

Registration link: (Last Date for Registration: 18th JUNE, 2014)

<https://docs.google.com/a/gtu.edu.in/forms/d/1-ukCKDjZEkKos-MR55jOaIyQ6dl1Nt7GvpZfdPwzs/viewform>

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I/C Registrar