Report:Confluence: The Art & Science of Designing Question Papers(Dt:31/01/2015)

A Workshop: FOR B.E SEMESTER 3 & 4 GROUP 2 : (Bio-Medical Engineering, Instrumentation & Control Engineering, Computer Engineering,Information Technology, Computer Science & Engineering, Information &CommunicationTechnology, Electrical & Electronics Engineering, Electronics Engineering, Electronics &Communication Engineering, Electronics & Telecommunication Engineering, ElectricalEngineering, Power Electronics Engineering.) was arranged on,

Date: Saturday, 31stJanuary, 2015 At GTU's Chandkheda Campus, Sabarmati-Koba Highway, Nr. Visat   Three Roads,   Ahmedabad

As Badly set or unbalanced question papers can put even the most carefully designed syllabi to shame and can act as a dis-incentive for attending classes and may not permit a great learning environment to develop it is very important to design a question paper rather than just setting a question paper..

GTU is initiating a new project – called 'Confluence: The Art & Science of Designing Question Papers'- with the objective of (i) designing guidelines for setting good quality examination papers and (ii) for each subject, preparing two sample question papers, which can be used to measure a student's competence in the whole of the syllabus of the subject well.

The main agenda was to discuss the subject wise / chapter wise questions designed and brought along with by core committee members as well as the registered faculties attending the workshop , and finally to set the model question paper including the finalized questions.

Faculties from Electrical Engineering , Computer Engineering , Computer Science & Engineering , Bio-Medical Engineering Instrumentation &Control

Engineering , Power Electronics ,Electronics & Telecommunication Engineering ,Electronics & Communication Engineering ,Information Technology and Electronics Engineering attended the workshop .They learned about designing a balanced question paper. They decided to design the model papers of respective subjects, also they had feelings to share the discussions made in the workshop with their colleagues so that the actual purpose of the workshop can be served.

The following faculties attended the workshop.

SR. NO.	Name	Institue Name	Branch
1	K P Parmar	Shri J M Sabva Institute of Engineering & Technology-Botad	09 Electrical Engineering
2	PANCHAL ESAN P.	D.A. DEGREE ENGINEERING & TECHNOLOGY	07 Computer Engineering
3	SHAKUNTLA RAVANI	PACIFIC SCHOOL OF ENGINEERING	31 Computer Science & Engineering
4	Siba Ram Raut	Laxmi Institute Of Technology,Sarigam	31 Computer Science & Engineering
5	Ghanshyam D Parmar	GEC Gandhinagar	03 Bio-Medical Engineering
6	Sheetal Shah	Venus International College Of Technology	07 Computer Engineering
7	UTPAL PANDYA	Sarvajanik College of Engineering & Technology	17 Instrumentation &Control Engineering
8	Dr.Hiren H. Patel	Sarvajanik College of Engineering and Technology, Surat	09 Electrical Engineering
9	N. D. Mehta	VGEC, Chandkheda	24 Power Electronics Engineering
10	KeyurBrahmbhatt	BVM Engineering College	16 Information Technology
11	KINJAL	VGEC	11 Electronics & Communication Engineering
12	Dr.Bhargav C. Goradiya	Birla VishvakaramMahavidyalaya	12 Electronics & Telecommunication Engineering

13	Dave Shivam K.	H J D Institute, Kera-Kutch	09 Electrical Engineering
14	KAPIL ARORA	PARUL INSTITUTE OF TECHNOLOGY	09 Electrical Engineering
15	GEET GOVIND PARIDA	PARUL INSTITUTE OF TECHNOLOGY	09 Electrical Engineering
16	KIRAN THAKKAR	PARUL INSTITUTE OF TECHNOLOGY(087)	31 Computer Science & Engineering
17	NIDHI TRIVEDI	PARUL INSTITUTE OF TECHNOLOGY(087)	31 Computer Science & Engineering
18	Bhupendra Patel	Dr.Jivraj Mehta Institute Of Technology (082)	31 Computer Science & Engineering
19	TanviVarma	parul institute of technology	31 Computer Science & Engineering
20	BHAILAL LIMBASIYA	PARUL INSTITUTE OF TECHNOLOGY	31 Computer Science & Engineering
21	Anirudha Singh	Dr.Jivraj Mehta Institute of Technology.	11 Electronics & Communication Engineering
22	Sunil A Bakhru	BVM Engg College	07 Computer Engineering
23	M. P. Prajapati	BVM Engg College	10 Electronics
24	Ajay M. Patel	BVM Engg College	09 Electrical Engineering
25	ChiragThakar	LDCE	16 Information Technology
26	Dr S N Pandya (Coordinator)	LDCE	24 Power Electronics Engineering
27	Prof V G Patel	VGEC	24 Power Electronics Engineering
28	Dr BhavikSuthar (Coordinator)	LDCE	09 Electrical Engineering