Gujarat Technological University - Ahmedabad

RefNo.GTU/ME_Exam//VLSI.Embd./25112011/11602   Dt. 30/11/2011

ME Sem - I (VLSI & Embedded System Design :052) Time Table- Winter Exam - 2011

(A) Theory

Subject Code	Subject Name	College Name	Exam Date	Exam Centre
715201	Semiconductor Device Modeling	GTU - Ahmedabad	11/01/2012	VGEC, Chandkheda, Ahmedabad
715202	Digital VLSI Design - I	GTU - Ahmedabad	13/01/2012	VGEC, Chandkheda, Ahmedabad
715203	Embedded System Software	GTU - Ahmedabad	16/01/2012	VGEC, Chandkheda, Ahmedabad
715204	Advance Computer Architecture	GTU - Ahmedabad	18/01/2012	VGEC, Chandkheda, Ahmedabad
710001	Communications Skills & research	GTU - Ahmedabad	20/01/2012	VGEC, Chandkheda, Ahmedabad

(B) Practical/Lab

Subject Code	Subject Name	College Name	Exam Date	Exam Centre
715207	Semiconductor Device Modeling	GTU - Ahmedabad	02/01/2012	VGEC, Chandkheda, Ahmedabad
715208	Embedded System Software	GTU - Ahmedabad	04/01/2012	VGEC, Chandkheda, Ahmedabad
715209	Digital VLSI Design - I	GTU - Ahmedabad	06/01/2012	VGEC, Chandkheda, Ahmedabad

Important Instructions for Students appearing in above mentioned examinations:

1. All students of ME (VLSI and Embedded System) are instructed to remain Present at exam center as per the Schedule

2. All the students are required to carry hall ticket with them issued by the university during exam time.

3. They must be in their respective examination centre 30 minutes before the exam time failing which entry will not be permitted.

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I/C Controller of Examination