

GUJARAT TECHNOLOGICAL UNIVERSITY
AND
SEER-AKADEMI PVT.LTD
M.E. PROGRAM 2012
In
VLSI & Embedded Systems Design
Code: 52



Date: 4-12-2012

M.E. SEMESTER-1 VLSI & EMBEDDED SYSTEMS DESIGN
PRACTICAL EXAM TIME-TABLE DECEMBER 2012

SUBJECT CODE	SUBJECT NAME	DATE	TIME
715209	Digital VLSI Design I- Frontend	24-12-2012	10:30 to 1:30
715208	Embedded Systems Software Design	26-12-2012	10:30 to 1:30
715207	Semiconductor Device Modeling	27-12-2012	10:30 to 1:30

Exam Center: GNLU, Sector-26, Gandhinagar.

Important Instructions for Students appearing in above mentioned examinations:

1. All students of above ME courses are instructed to remain Present at exam center 30 minutes before the exam time failing which entry will not be permitted.
2. All the students are required to carry hall ticket with them issued by the university during exam time.

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I/C controller of Examination