

# GUJARAT TECHNOLOGICAL UNIVERSITY

**M.E. EC (VLSI System Design) (W.E.F July 2012)**

**Proposed Teaching Scheme (Semester-III)**

Subject Code	NAME OF SUBJECT	TEACHING SCHEME(HOURS)			CREDITS
		THEORY	TUTORIAL	PRACTICAL	
	Major Elective IV	4	2	0	5
734201	ASIC Design Lab	0	0	4	2
730001	Seminar	0	0	4	2
730002	Presentation of Literature Review	-	-	-	2
730003	Dissertation Phase-I	0	0	18	9
	<b>Total</b>	<b>4</b>	<b>2</b>	<b>24</b>	<b>20</b>

## MAJOR ELECTIVE – IV

Subject Code	Subject Name
734202	IC Fabrication Technology
734203	High Speed CMOS VLSI Circuit