GUJARAT TECHNOLOGICAL UNIVERSITY
M.E. (Embedded Systems) (54)
TEACHING SCHEME

SEM-III					
Subject Code	**Subject Name**	**Teching Scheme (Hours)**			**Credits**
		Theory	**Tutorial**	**Practical**	
735401	Embedded Project Development Laboratory	0	0	4	2
	Major Elective - IV	4	0	2	5
730001	Seminar	0	0	4	2
730002	Presentation of Literature Review	_	_	_	2
730003	Dissertation Phase-I	0	0	18	9
	TOTAL	**4**	**0**	**28**	**20**

Major Elective IV

Subject Code	**Major Elective - IV**
735402	Digital Signal Processor Architecture and Applications
735403	Cortex Processor Architecture and Programming